

FIG. 1A

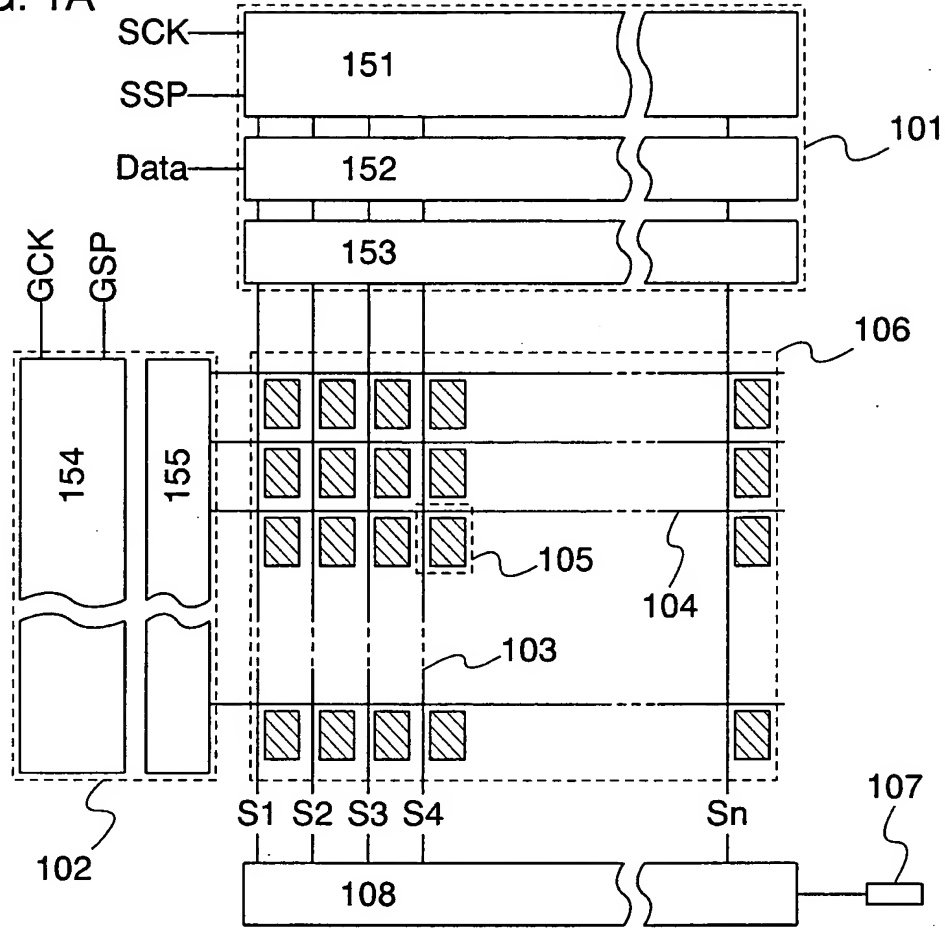


FIG. 1B

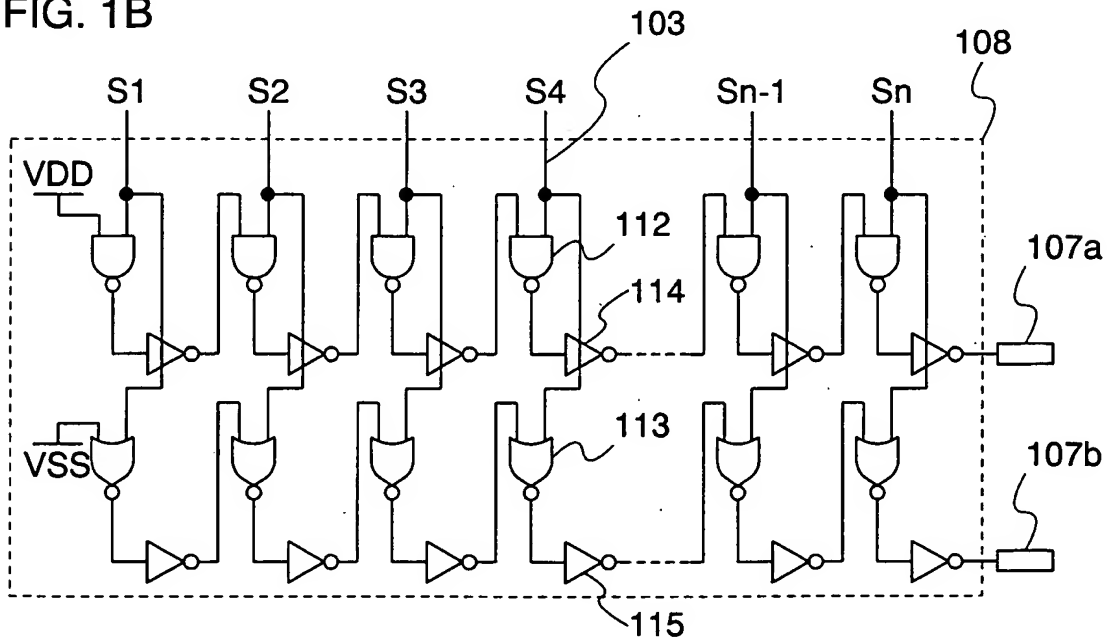


FIG. 2

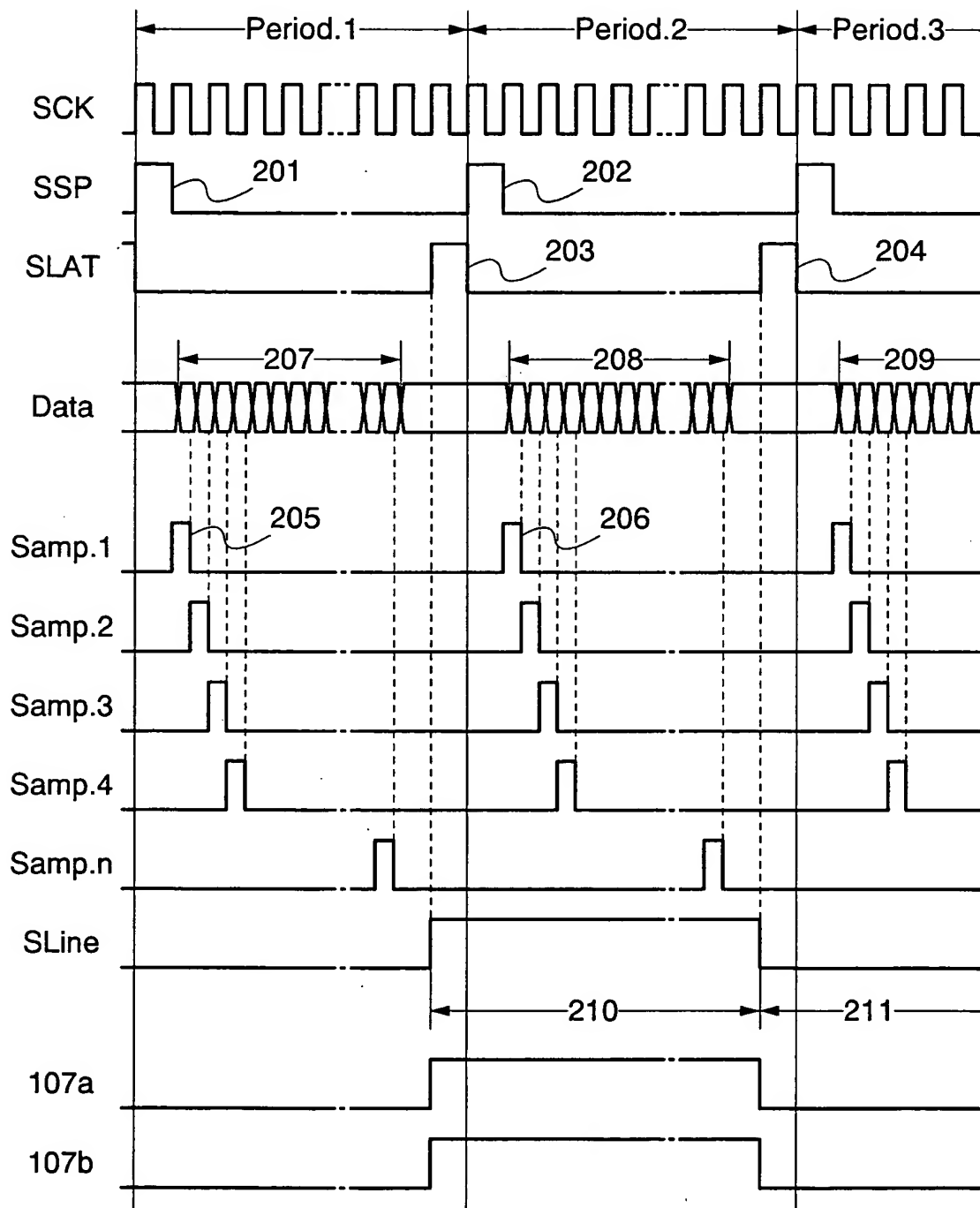


FIG. 3A

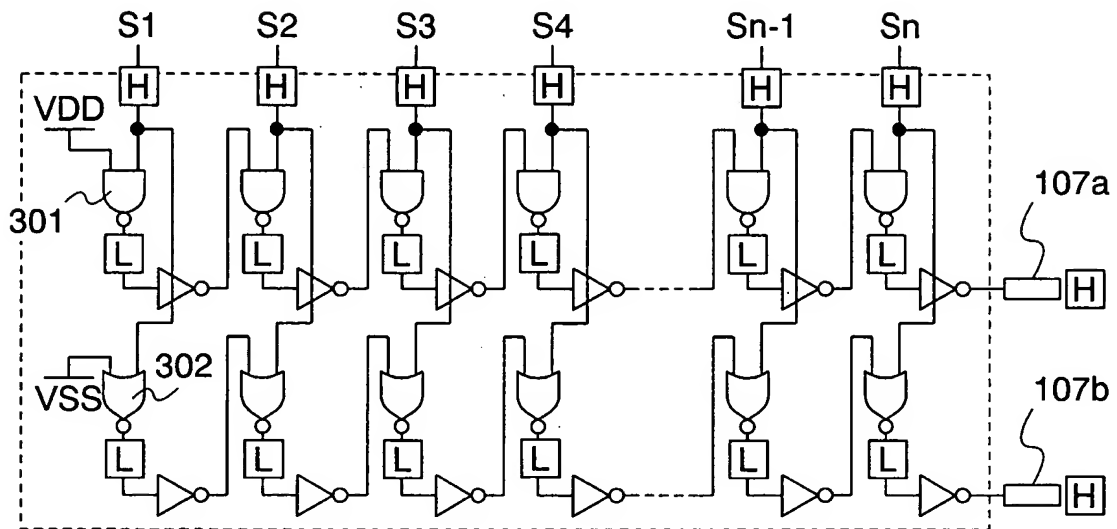
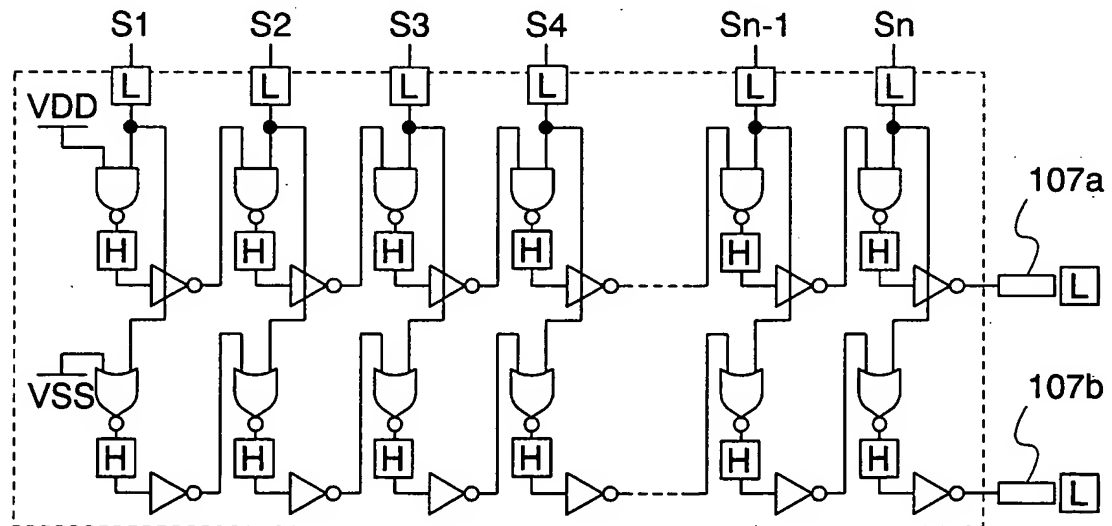


FIG. 3B



Defect operation mode A

FIG. 4A

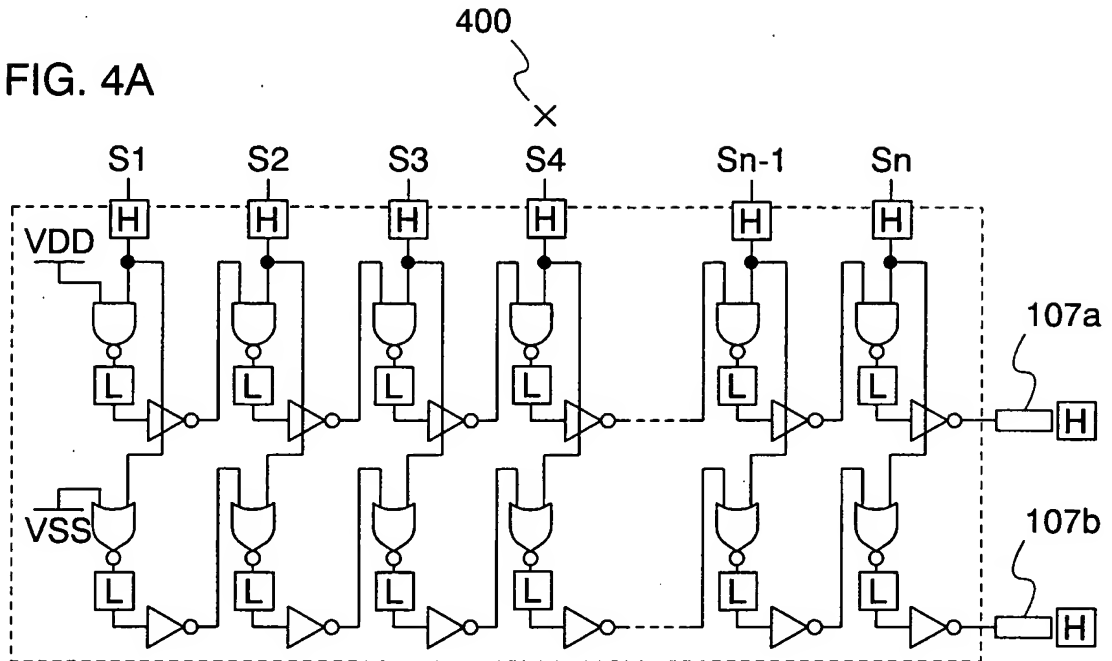
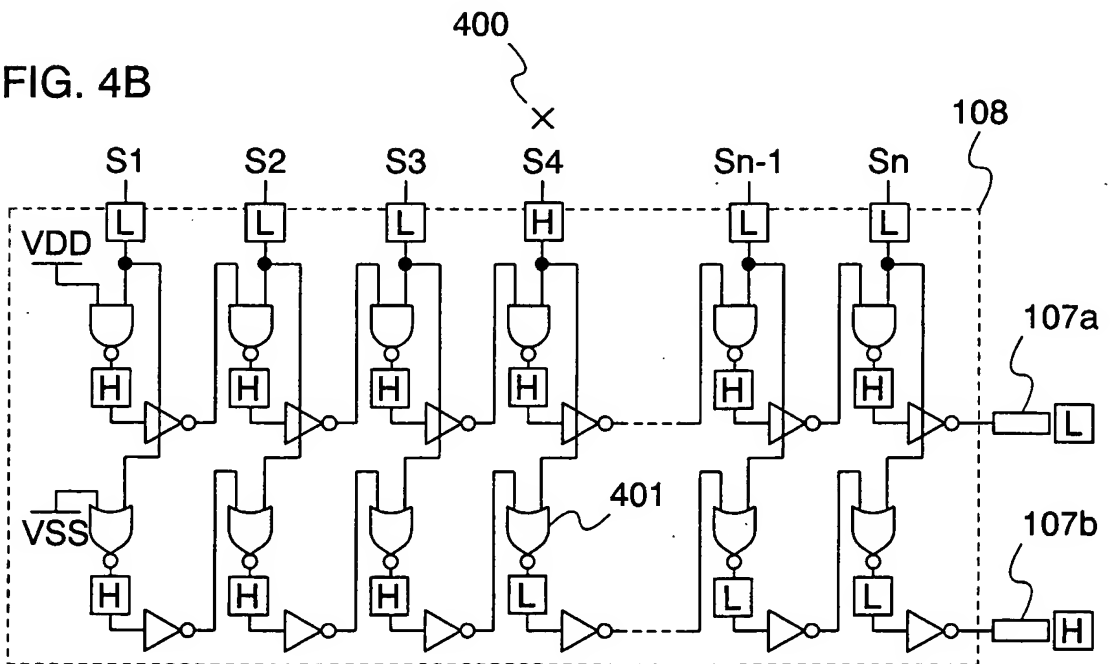
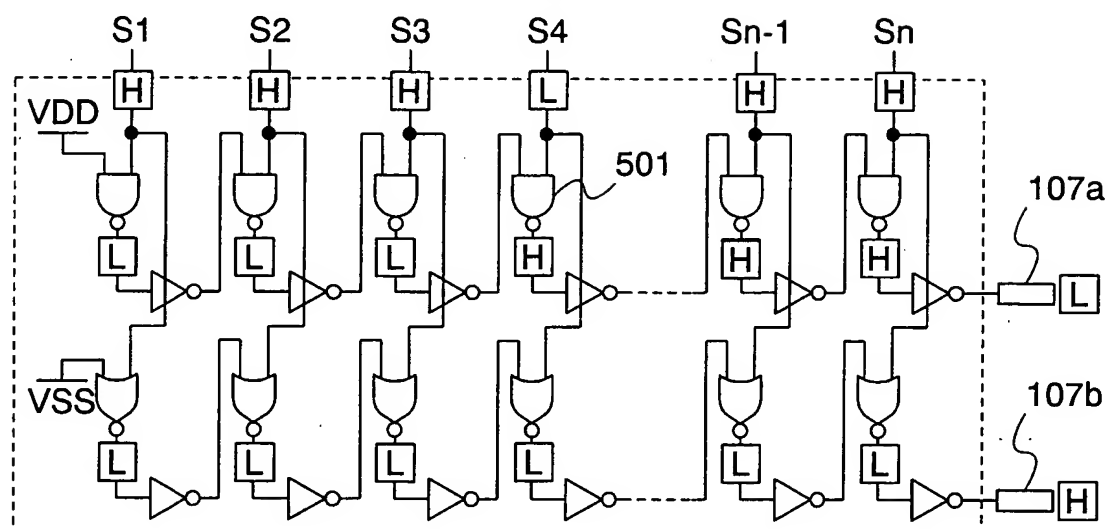


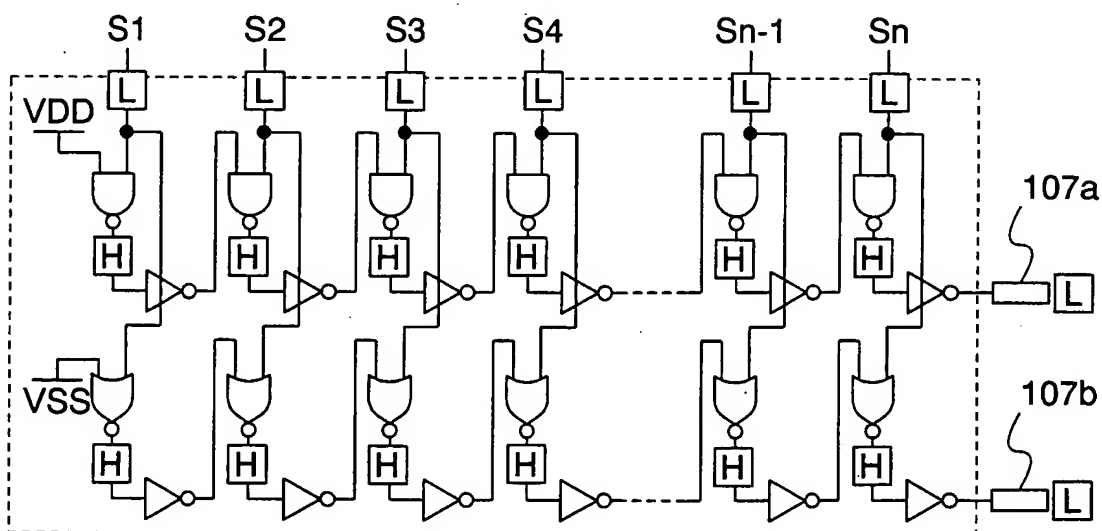
FIG. 4B



500



500



Defect operation mode C

FIG. 6A

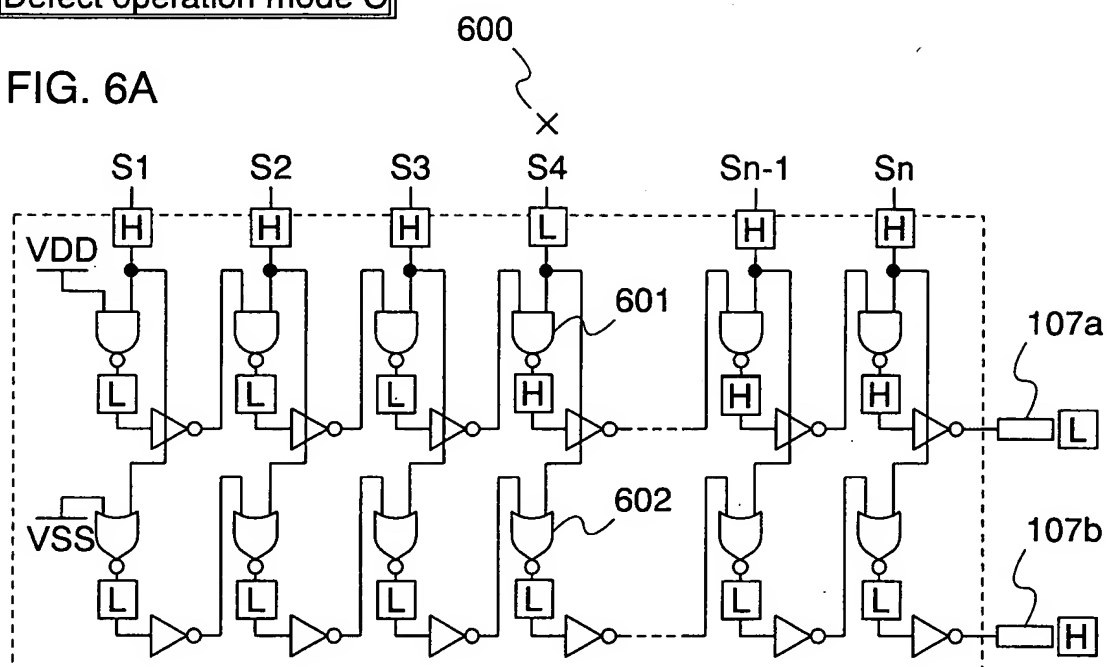
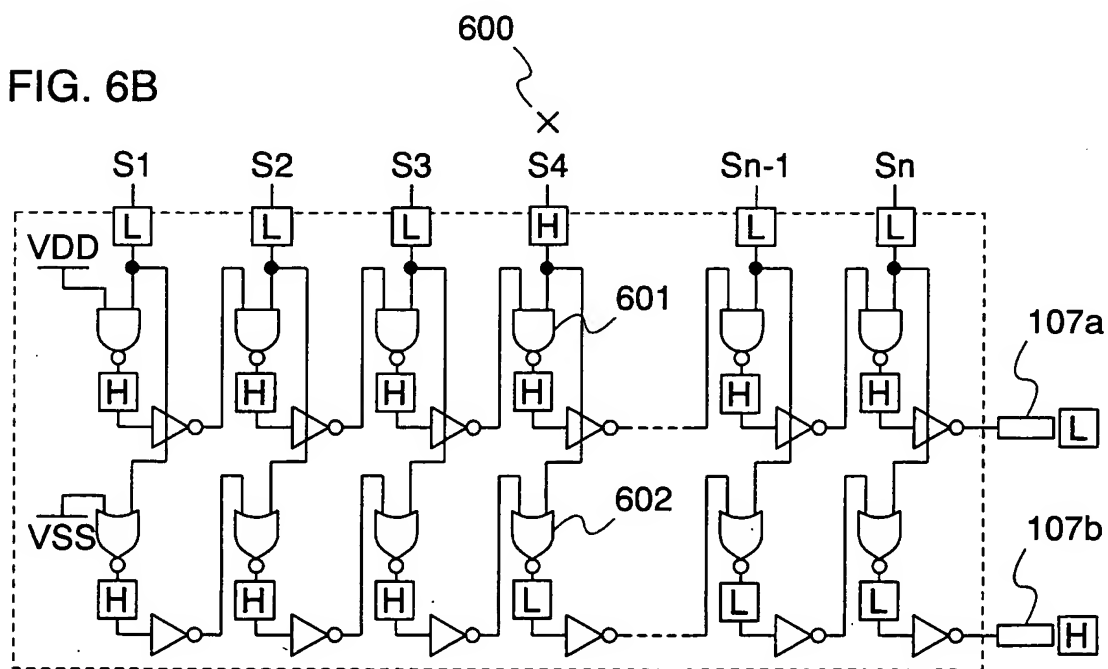
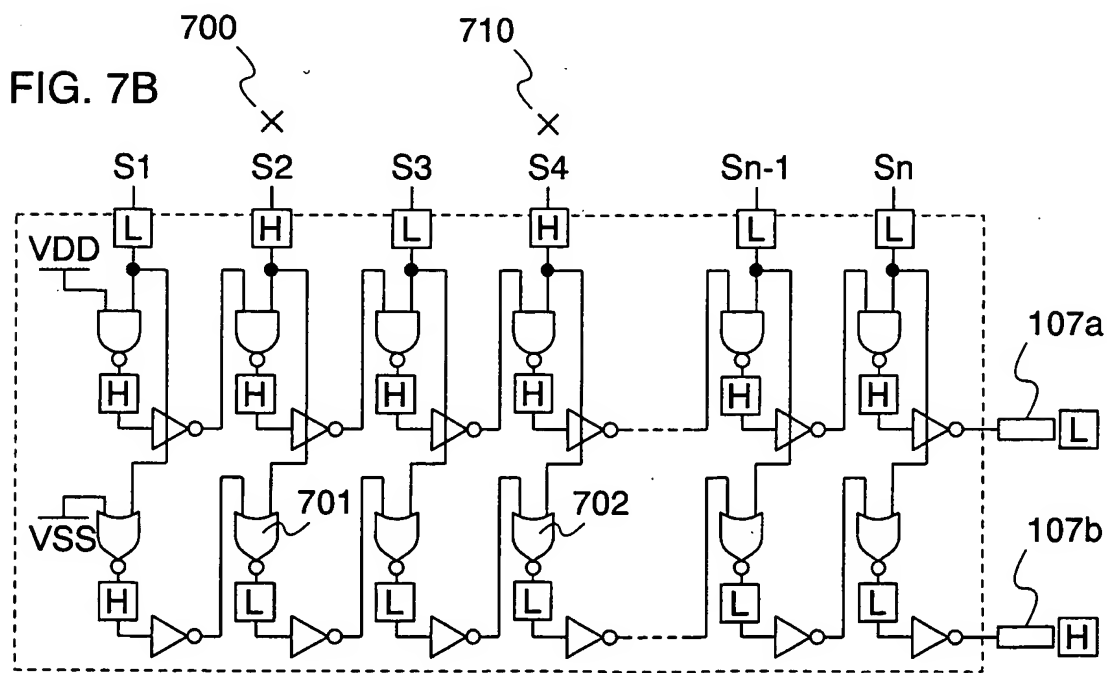
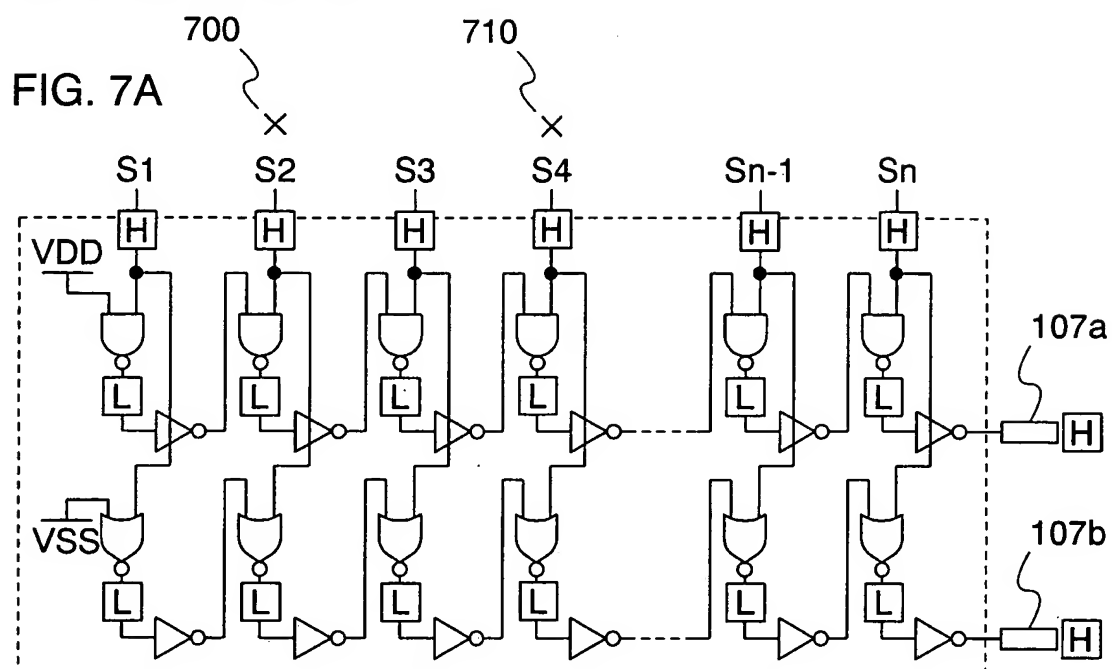
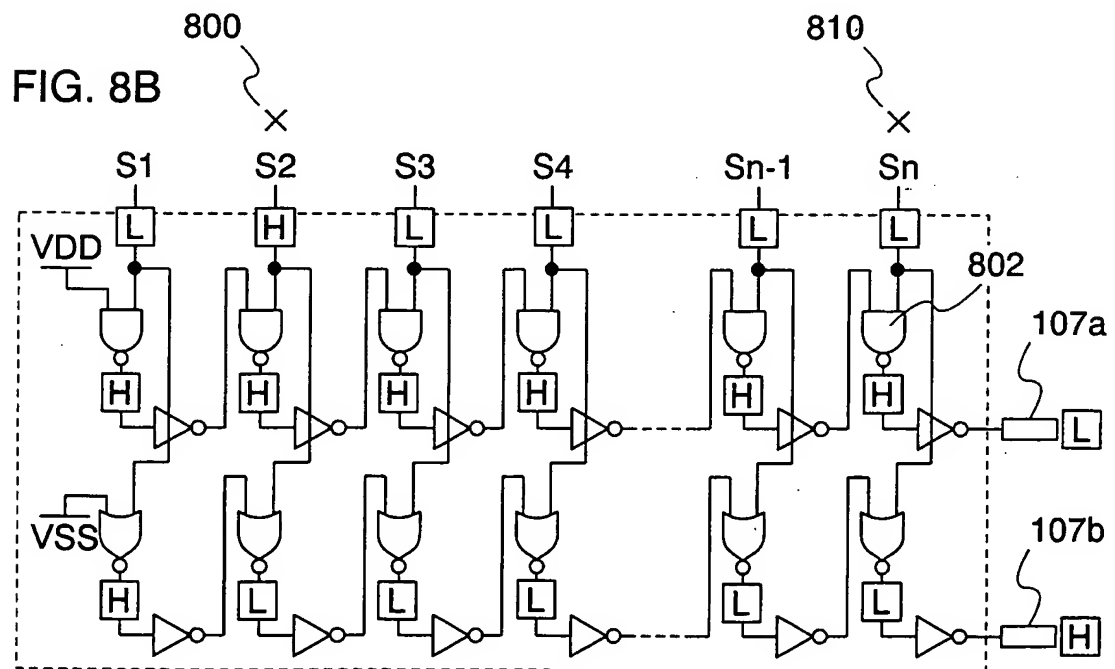
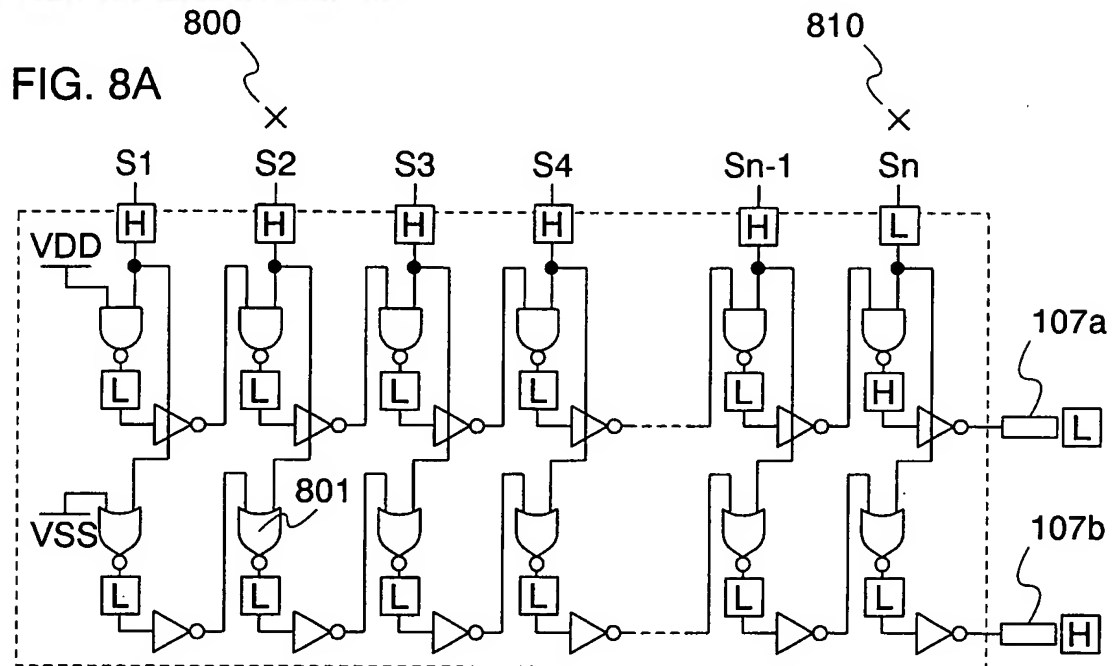


FIG. 6B



Defect operation mode D

Defect operation mode E

Defect operation mode F

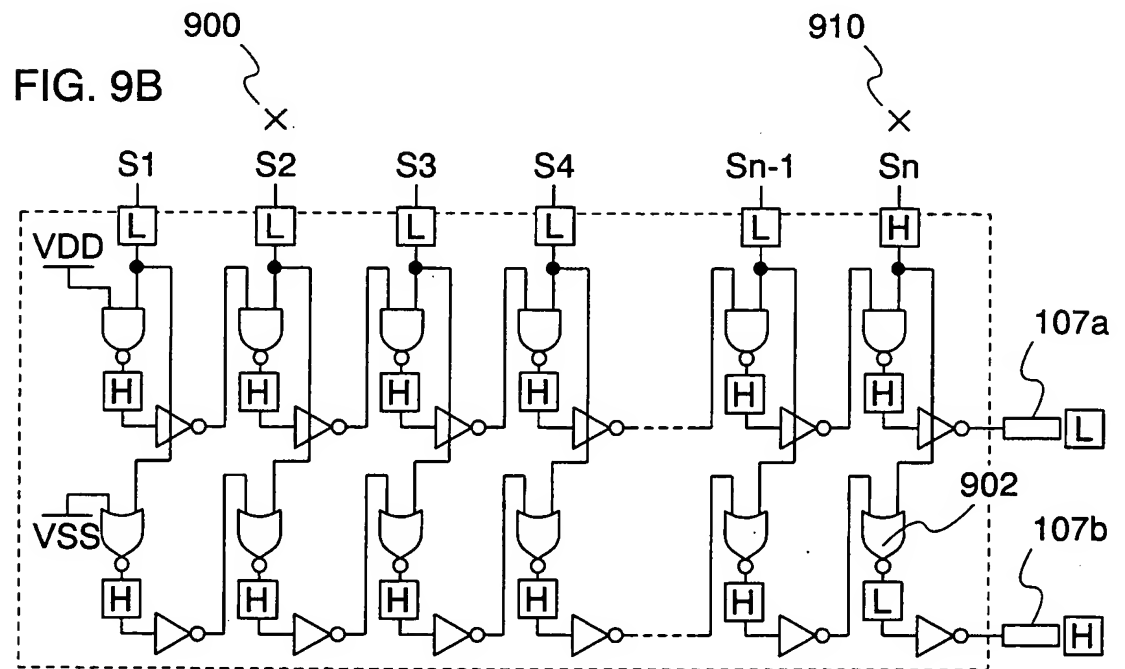
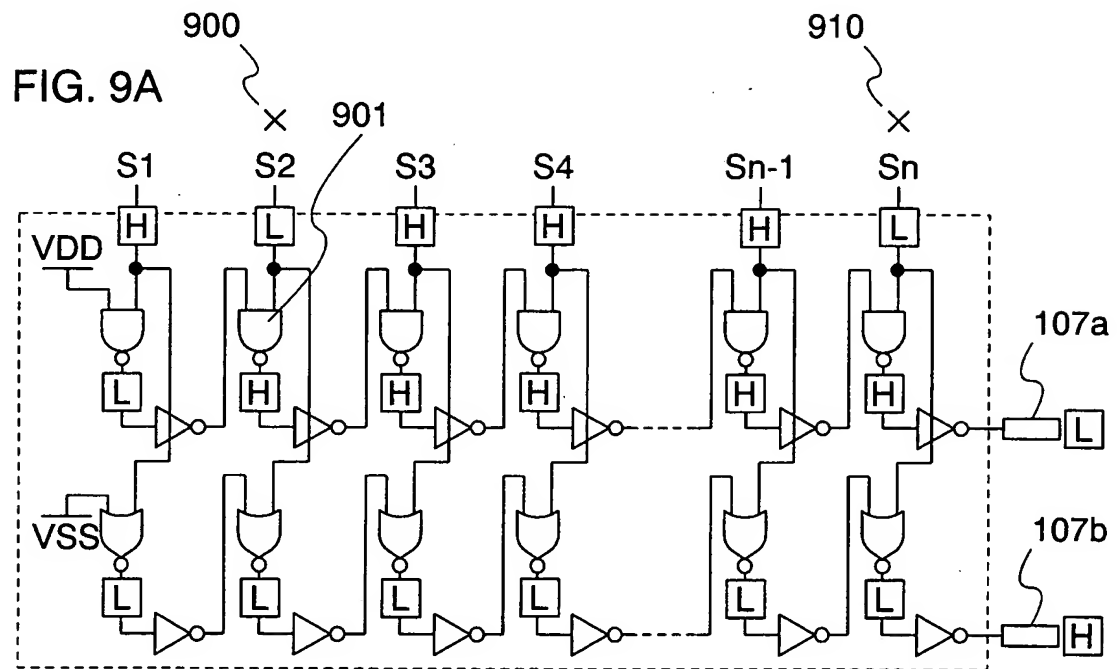


FIG. 10A

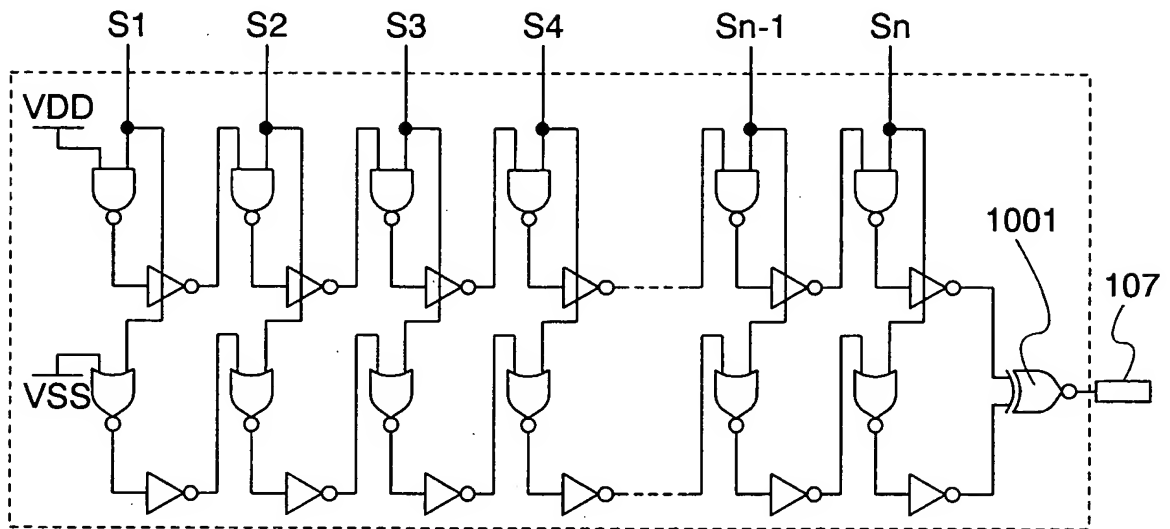
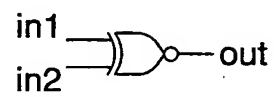


FIG. 10B



in1	in2	out
H	H	H
H	L	L
L	H	L
L	L	H

FIG. 11

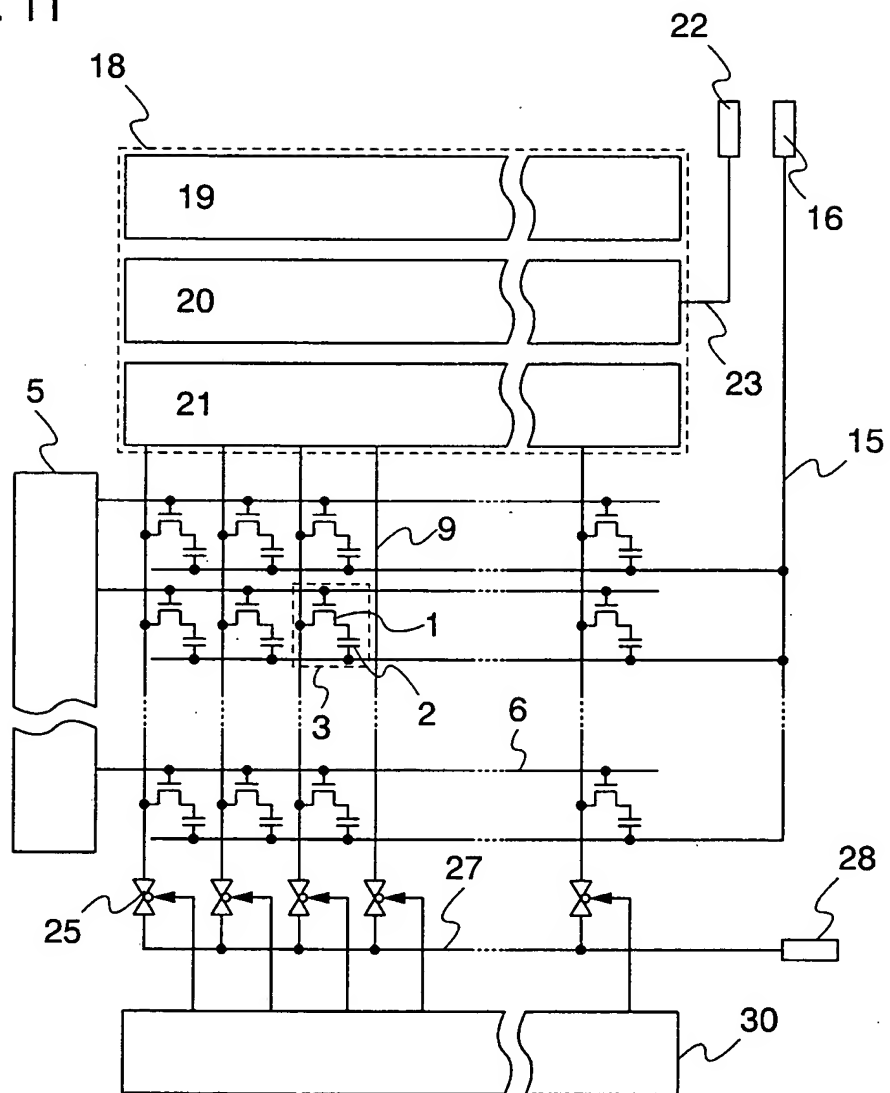


FIG. 12A

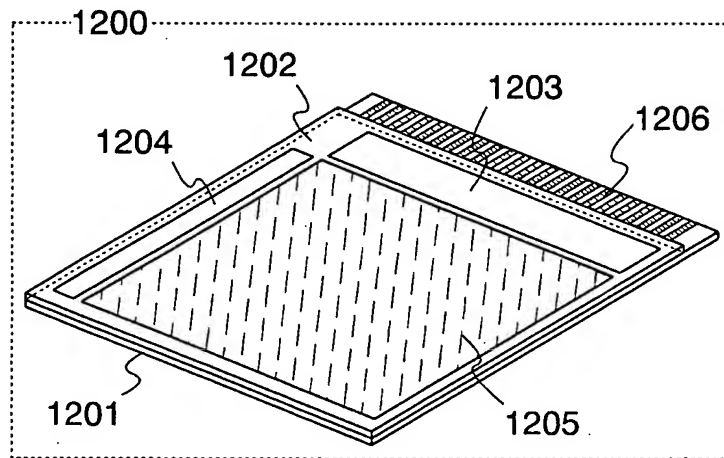


FIG. 12B

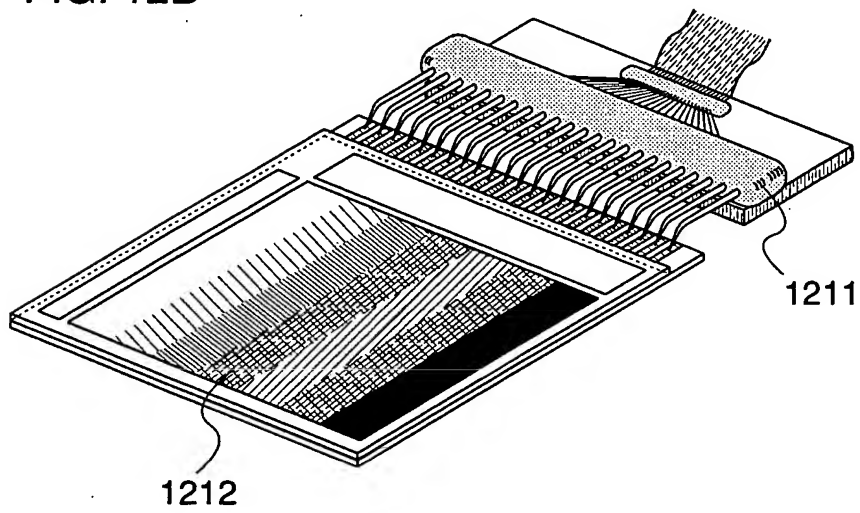


FIG. 13

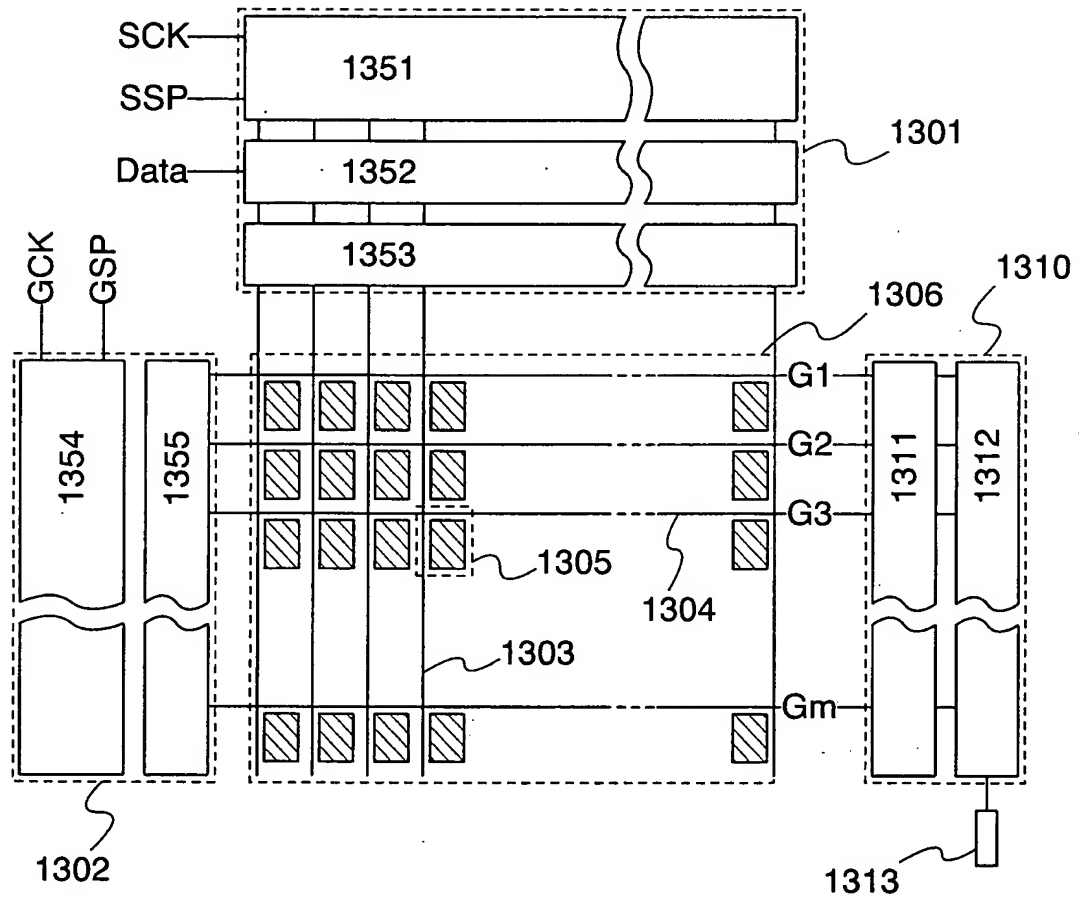


FIG. 14

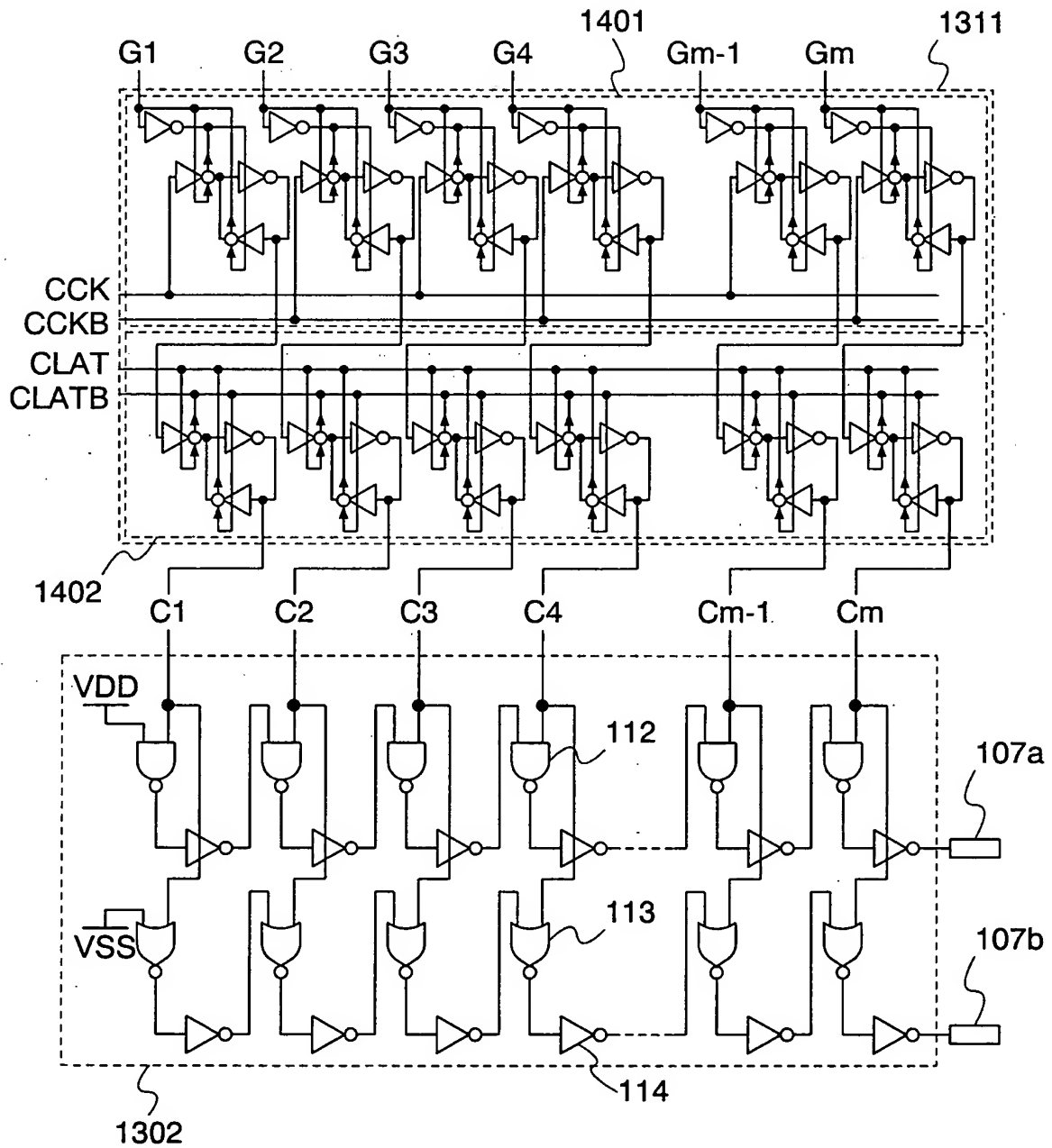


FIG. 15

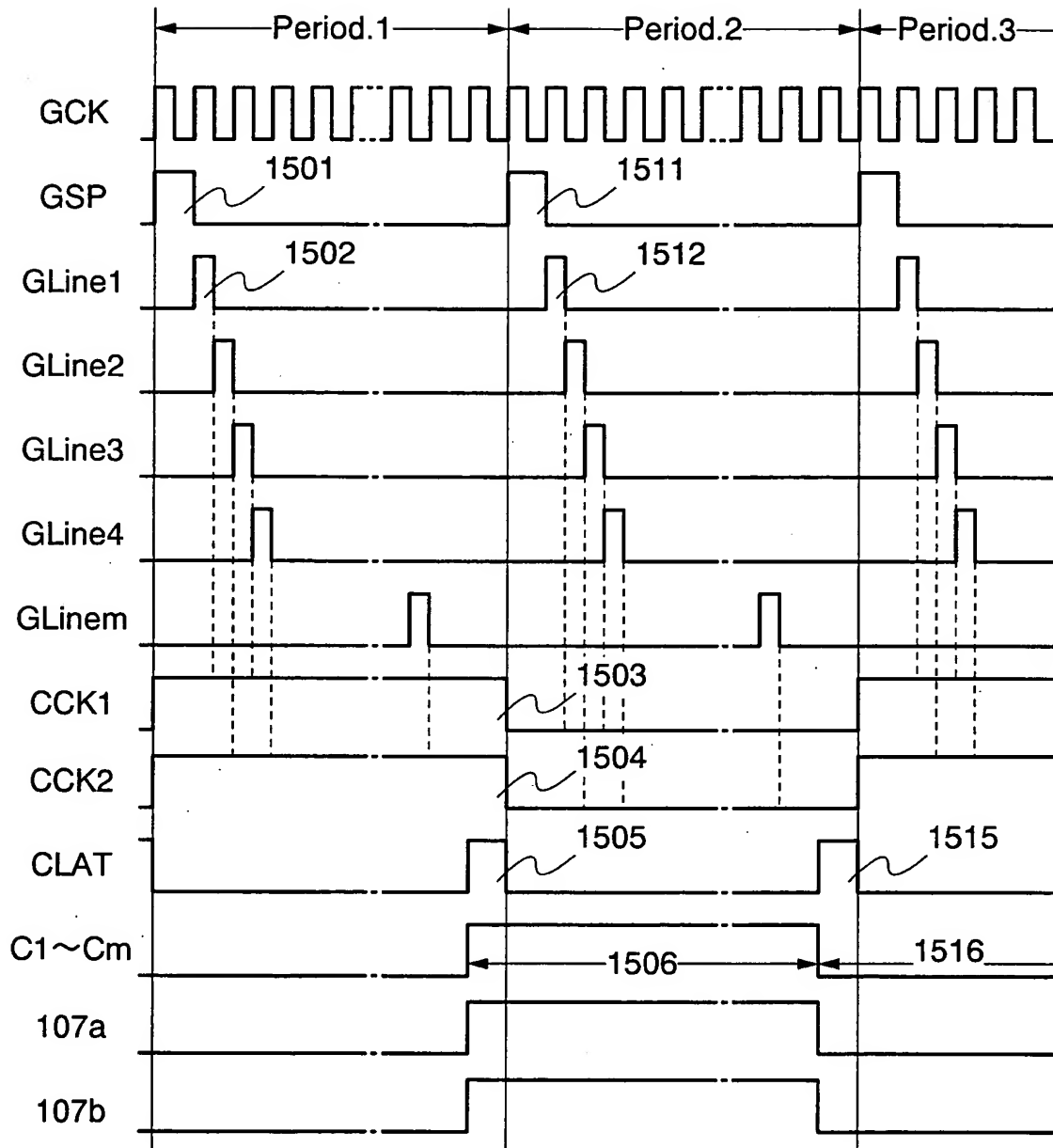


FIG. 16

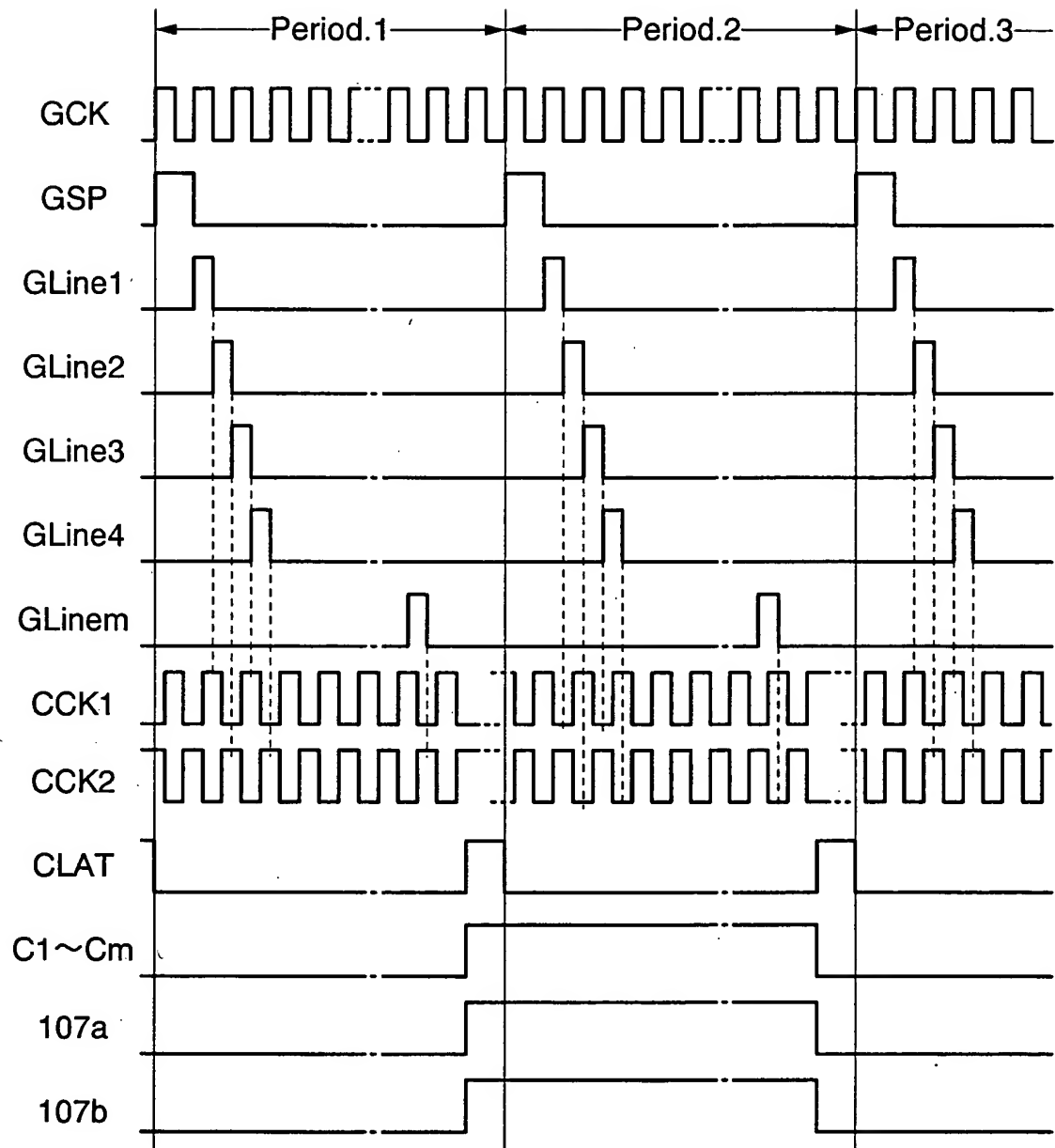


FIG.17

